Ref #	Hits	Search Query	DBs	Default Operator	Plurals
# L1	3618579	(semiconductor die chip ic (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L2	49061	(leadframe (lead adj frame)) same 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L3	8668	(paddle ((chip die) adj4 (pad paddle))) same 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L4	5031	wire same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L5	4979	lead and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON
L6	2729	(support supporting supportable supported) and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON
L7	2513	(package packaging packaged) and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT IBM_TDB		ON
L8	2334	(encapsulating encapsulate encapsulant encase encapsulated resin epoxy) and 7	US-PGPUB USPAT; USOCR; EPO; JPO; DERWENT IBM_TDB		ON

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1976744 (substrate carrier board ((printed US-PGPUB; OR USPAT; Wiring cirucit) adj4 board)) and "9" USPAT; EPO; PPO; DERWENT; BM_TDB (substrate carrier board ((printed US-PGPUB; ON Wiring cirucit) adj4 board)) and 8 USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB (IBM_TDB)	1976744 (substrate carrier board ((printed wiring cirucit) adj4 board)) and "9" USOCR; EPO; JPO; DERWENT; IBM_TDB wiring cirucit) adj4 board)) and 8 USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB IBM_TDB IBM_TDB				4 .	·	#	
1976744 (substrate carrier board ((printed wiring cirucit) adj4 board)) and "9" USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB wiring cirucit) adj4 board) and 8 USPAT; U	Light 1976744 (substrate carrier board ((printed US-PGPUB); Programming circutt) adj4 board)) and "9" USPAT; USOCR; EPO, JPO; DERWENT; USPAT;	NO	O					
wiring cirucit) adj4 board)) and "g" (substrate carrier board ((printed wiring cirucit) adj4 board)) and 8 wiring cirucit) adj4 board)) and 8	L10 1976744 (substrate carrier board ((printed wiring cirucit) adj4 board)) and "g" wiring cirucit) adj4 board)) and 8 wiring cirucit) adj4 board)) and 8	8	OR					
wiring cirucit) adj4 board)) and "9" [Substrate carrier board ((printed wiring cirucit) adj4 board)) and 8 [Substrate carrier board ((printed wiring cirucit) adj4 board)) and 8	1976744 (substrate carrier board ((printed wiring cirucit) adj4 board)) and "9" [10 1948 (substrate carrier board ((printed wiring cirucit) adj4 board)) and 8 [10 1948 (substrate carrier board)]	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB					
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1976744	L10 1948	(substrate carrier bo wiring cirucit) adj4 t	(substrate carrier bo wiring cirucit) adj4 t		·			
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